



# Republic of Yemen

Ministry of Higher Education & Scientific Research  
Council of Academic Accreditation & Quality



Assurance of Higher Education (CAQA)

21 September University for medical and Applied Science

Faculty of Engineering and Computer  
Department of Biomedical Engineering  
Program of Biomedical Engineering

Course Specification of  
Microprocessors & Microcontrollers  
Course Code. (07.02.722)

2024



T4: This Template is Developed and Approved by CAQA-Yemen, 2023

Prepared by:  
Assoc. Prof. Farouk Al-  
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Reviewed by:  
Dr. ----

Head of the Department:  
Dr. Awadh Al-Kubati

Quality Unit:  
Dr. Mohammed Al-  
Shamahi

## I. General Information:

1.	Course Title:	Microprocessors and Microcontrollers				
2.	Course Code:	07.02.722				
3.	Credit Hours:	Credit Hours	Theory Contact Hours		Practical Contact Hours	
			Lecture	Tutorial /Seminar	Lab	Clinical
		3	2	--	2	--
4.	Level/ Semester at which this Course is offered:	--- Level / --- Semester				
5.	Pre –Requisite (if any):	Logic Circuits & Programming I				
6.	Co –Requisite (if any):	-----				
7.	Program (s) in which the Course is Offered:	Bachelor of Biomedical Engineering				
8.	Language of Teaching the Course:	Mixed (Arabic & English)				
9.	Location of Teaching the Course:	Faculty of Medical Technology				
10.	Prepared by:	Assoc. Prof. Farouk Al-Fahaidy				
11	Date and Number of Approval by Council:					

## II. Course Description:

This course aims to introduce the basic concepts and principles of microprocessors (MPs) and microcontrollers (MCs) based on their architectures, programming, and applications in the field of biomedical engineering. The MP/MC plays the main processing and controlling unit in different biomedical instrumentations. Topics covers, an introduction to MP/MC architectures & units, the 8086/8088-Mps internal architecture, the 8088-MP IC interfacing and assembly programming, the 8051-MC's GPIO ports interfacing & features and assembly/Micro-C programming. Throughout

practical Lab experiments & computer-based lab works as well as, course project work, students will develop their practices skills in simulation, programming and implementation of context-based systems using the MP/MC-based systems.

III. Course Intended Learning Outcomes (CILOs) : Upon successful completion of the course, students will be able to:		Referenced PILOs		
<b>A. Knowledge and Understanding:</b>		I, P or M/A		
a1	Explain basic concepts and principles of MPs/MCs-based systems design, programming and applications in the field of biomedical engineering and healthcare environment.	I	A1	Explain the appropriate models, theories, mathematical foundations, and techniques related to biomedical engineering technology context.
a2	Describe the theories and internal architecture of the microprocessors/ microcontrollers as well as their differences, features & capabilities.	I	A2	Clarify the biomedical devices maintenance principles and how these are important for solving biomedical devices and equipment's problems in health environment.
<b>B. Intellectual Skills:</b>				
b1	Solve healthcare domain problems related to MPs/MCs-based systems using suitable software packages, programming language, and proper electronical elements, devices & ICs.		B2	Analyze the impacts of problems related to the Biomedical equipments and its solution principles in a creative manner by using a systematic and analytical thinking methods.
<b>C. Professional and Practical Skills:</b>				
c1	Use different programming environments & IDE in the development & integrating of controlling programs and for simulation of the suggested solutions while solving practices	P	C2	Evaluate an engineering technique, modern analytical tools and required computer programs to analyzing and solve the problems of medical

	problems related to healthcare fields.			devices.
c2	Carry-out lab & environmental experiments for maintenance and experimentation in biomedical engineering practices.	P	C3	Develop an engineering approach, engineering equipment, instruments to maintenance and conduct experiments, and present results in the biomedical engineering practice.
<b>D. Transferable Skills:</b>				
d1	Demonstrate the ability to work in stressful environments within different constraints	A	D1	Function effectively as an individual, team member, or leader in activities relevant to biomedical engineering, and collaborating to achieve a shared objective.
d2	Communicate well in both orally and in written forms.	M	D2	Acquire entrepreneurial skills and effectively manage tasks, time, processes and resources.
I= Introduced, P=Practiced or M/A= Mastered/Advanced				

<b>(A) Alignment of Course Intended Learning Outcomes (Knowledge and Understanding) to Teaching Strategies and Assessment Methods:</b>			
	Course Intended Learning Outcomes	Teaching Strategies	Assessment Strategies
a1	Describe the theories and internal architecture of the microprocessors/ microcontrollers as well as their differences, features & capabilities.	<ul style="list-style-type: none"> <li>Interactive lectures &amp; examples,</li> <li>Interactive class discussions,</li> <li>Exercises and home works.</li> </ul>	<ul style="list-style-type: none"> <li>Written tests (mid and final terms and quizzes),</li> <li>Coursework activities assessment,</li> <li>Home works and assignments.</li> </ul>
a2	Explain basic concepts and principles of MPs/MCs-based systems design, programming and applications in the field of		

	biomedical engineering and healthcare environment.		
<b>(B) Alignment of Course Intended Learning Outcomes (Intellectual Skills) to Teaching Strategies and Assessment Methods:</b>			
	<b>Course Intended Learning Outcomes</b>	<b>Teaching Strategies</b>	<b>Assessment Strategies</b>
b1	Solve healthcare domain problems related to MPs/MCs-based systems using suitable software packages, programming language, and proper electronical elements, devices & ICs.	<ul style="list-style-type: none"> <li>▪ Interactive lectures &amp; examples,</li> <li>▪ Interactive class discussions,</li> <li>▪ Case studies,</li> <li>▪ Exercises and home works,</li> <li>▪ Laboratory/Practical experiments-based session,</li> <li>▪ Computer laboratory-based sessions,</li> <li>▪ Directed self- study,</li> <li>▪ Problem based learning,</li> <li>▪ Team work (cooperative learning),</li> <li>▪ Project.</li> </ul>	<ul style="list-style-type: none"> <li>▪ Written tests (mid and final terms and quizzes),</li> <li>▪ Lab\Project report</li> <li>▪ Practical lab performance assessment,</li> <li>▪ Coursework activities assessment,</li> <li>▪ Home works and assignments,</li> <li>▪ Presentations.</li> </ul>
<b>(C) Alignment of Course Intended Learning Outcomes (Professional and Practical Skills) to Teaching Strategies and Assessment Methods:</b>			
	<b>Course Intended Learning Outcomes</b>	<b>Teaching Strategies</b>	<b>Assessment Strategies</b>
c1	Use different programming environments & IDE in the development & integrating of controlling programs and for simulation of the suggested solutions while solving practices problems related to healthcare fields.	<ul style="list-style-type: none"> <li>▪ Exercises and home works,</li> <li>▪ Laboratory/Practical experiments-based session,</li> <li>▪ Computer laboratory-based sessions,</li> <li>▪ Directed self- study,</li> <li>▪ Problem based learning,</li> <li>▪ Team work (cooperative learning),</li> <li>▪ Project.</li> </ul>	<ul style="list-style-type: none"> <li>▪ Lab\Project report</li> <li>▪ Practical lab performance assessment,</li> <li>▪ Coursework activities assessment,</li> <li>▪ Home works and assignments,</li> <li>▪ Presentations.</li> </ul>
c2	Carry-out lab & environmental experiments for maintenance and experimentation in		

	biomedical engineering practices.		
<b>(D) Alignment of Course Intended Learning Outcomes (Transferable Skills) to Teaching Strategies and Assessment Methods:</b>			
	<b>Course Intended Learning Outcomes</b>	<b>Teaching Strategies</b>	<b>Assessment Strategies</b>
d1	Demonstrate the ability to work in stressful environments within different constraints	<ul style="list-style-type: none"> <li>▪ Exercises and home works,</li> <li>▪ Case studies,</li> <li>▪ Laboratory/Practical experiments-based session,</li> <li>▪ Computer laboratory-based sessions,</li> <li>▪ Directed self- study,</li> <li>▪ Problem based learning,</li> <li>▪ Team work (cooperative learning),</li> <li>▪ Project.</li> </ul>	<ul style="list-style-type: none"> <li>▪ Lab\Project report</li> <li>▪ Practical lab performance assessment,</li> <li>▪ Home works and assignments,</li> <li>▪ Coursework activities assessment,</li> <li>▪ Presentations.</li> </ul>
d2	Communicate well in both orally and in written forms.		

#### IV. Course Contents:

##### A. Theoretical Aspect:

No.	Units/Topics List	Sub Topics List	Number of Weeks	Contact Hours	Learning Outcomes (CILOs)
1	Introduction to MP/MC & Computing	<ul style="list-style-type: none"> <li>– Course Orientations, Aims &amp; Objectives,</li> <li>– Introduction to Microprocessors &amp; Microcontrollers,</li> <li>– Numbering &amp; Coding Systems.</li> </ul>	1	2	a2
2	Internal Organization of Computer	<ul style="list-style-type: none"> <li>– Introduction to Internal Organization of Computer, Relation between Internal organization, Internal working of computer,</li> <li>– Brief history of the 80x86 family, Inside the 8088/8086.</li> </ul>	1	2	a1, a2
3	The 80x86 Microprocessor	<ul style="list-style-type: none"> <li>– Introduction to the Assembly Programming, Program</li> </ul>	1	2	a1, a2

No.	Units/Topics List	Sub Topics List	Number of Weeks	Contact Hours	Learning Outcomes (CILOs)
		Segments, – The 80x86-MP's Memory Addressing Modes, Logical, Offset & Physical Addresses Calculation, Demonstration with Examples.			
4	Assembly Language Programming	– Layout of Assembly Programs, Directives & A Sample Program, Assemble, link, and Run a Program, – Flag Register & Control Transfer Instructions, Data types and Definitions	1	2	a1, a2, b1
5	Arithmetic and Logic Instructions and Programs	– Unsigned Addition & Subtraction, – Unsigned Multiplication & Division, – Logical & Shift Instructions and Sample Programs.	1	2	a1, a2, b1
6	BCD, ASCII, Bios & Dos Programming in Assembly	– BCD & ASCII operands and Instructions, – Bios INT 10H Programming: using INT 10H, Dos Interrupt INT 21H: using INT 21H with different I/O Functions.	1	2	a1, a2, b1
7	Strings & Tables and Macros & Modular Programming	– String Instructions & Look-up Tables Programming, – Define Macro in Assembly, Define the Local Variable in Macro, and including the Macro in another File – Modular Programming, Advantages, Break a Large Program into Modules, Code the Modules and calling the Program, EXTRN Directive, PUBLIC Directive, Link a Subprograms into one Executable Program.	1	2	a1, a2, b1
8	Mid-Term Theoretical	– ALL Previous Topics	1	2	a1, a2, b1

No.	Units/Topics List	Sub Topics List	Number of Weeks	Contact Hours	Learning Outcomes (CILOs)
	Exam				
9	The 8086/8088 MP IC & Interfacing with the 8255-IC	<ul style="list-style-type: none"> <li>– The 8086/8088-MP IC, Pins Functions, Modes of Operations, Memory &amp; I/O Ports Addressing &amp; Address Decoding Circuits,</li> <li>– Interfacing &amp; Programming of the 8088-Mp with I/O peripherals such as, Switch, LEDs and Sensors</li> <li>– The 8255-PPI IC, IC Pins, Configuration and Interfacing with Keypad &amp; LCD.</li> </ul>	2	4	a1, a2, b1, b2
10	Introduction to the 8051 Microcontroller Family & Programming	<ul style="list-style-type: none"> <li>– Introduction to 8051 family MCs, IC for 8051, features like ports, Timers, and Serial Modules, Internal RAM, General Purpose Registers (GPRs), Special Function Registers (SFRs),</li> <li>– Basic Assembly Programming Instructions,</li> <li>– Flowchart standard symbols.</li> </ul>	1	2	a1, a2, b1
11	The 8051 Programming	<ul style="list-style-type: none"> <li>– Assembly Programming, Data Transfer Instructions, Addressing Modes, Data Processing Instructions, Program Branching Instructions, and Stack,</li> <li>– TIME DELAY Generation in 8051 MC.</li> </ul>	1	2	a1, a2, b1
12	The 8051 Interfacing with Assembly Programming	<ul style="list-style-type: none"> <li>– The 8051-MC Interfacing, I/O Ports, LEDs, Seven-Segments, Switches, Keypad, PWM, DC-motor, Stepper motor &amp; Alphanumeric LCD</li> <li>– ADC, DAC Modules.</li> </ul>	2	4	a2, b1, b2
13	The 8051-MC's Interrupts	<ul style="list-style-type: none"> <li>– Interrupts Programming, Steps in executing an interrupt, Interrupt Sources, Interrupt Vectors, Interrupt Enable (IE) register, External interrupt, Interrupt</li> </ul>	1	2	a2, b1, b2

No.	Units/Topics List	Sub Topics List	Number of Weeks	Contact Hours	Learning Outcomes (CILOs)
		Priorities.			
14	Final Theoretical Exam	– ALL Topics	1	2	a1, a2, b1, b2
Number of Weeks /and Units Per Semester			16	32	

### B. Practical Aspect (Lab/Clinical) (if any):

No.	Tasks/ Experiments	Number of Weeks	Contact Hours	Learning Outcomes (CILOs)
1	MP Lab & Computer based Lab Orientation: – Lap equipment and simulation Tools orientations – Installation of 8086/8088-Mps simulators and Proetus Simulator.	1	2	a2, b1, c1
2	– MOV and ADD assembly instructions illustrating memory addressing modes	1	2	b1, c1, d1
3	– Conditional Assembly Instructions, – Arithmetic, Shift & Logic & Shift Assembly Instructions.	2	4	b1, c1, c2. d1
4	– BCD, ASCII, BIOS & Dos Assembly	1	2	b1, c1, d1
5	– String Assembly Instructions & Lookup Tables Programming	1	2	b1, c1, d1
6	– Midterm Practical Exam	1	2	b1, c1, c2, d1
7	– Assembly programming: Macros and Modular Programming, – Building Assembly Program with Subroutines in Modular Programming.	2	4	b1, c1, c2, d1, d2
8	– The 8086/8088-Mps interfacing with Peripherals & Programming.	2	4	b1, c1, c2, d1,

No.	Tasks/ Experiments	Number of Weeks	Contact Hours	Learning Outcomes (CILOs)
				d2
9	<ul style="list-style-type: none"> <li>- The 8051-MC Interfacing and Programming with Peripherals,</li> <li>- Demonstrating Interrupts Programming with 8051-MC.</li> </ul>	2	4	b1, c1, c2, d1, d2
10	<ul style="list-style-type: none"> <li>- Projects Presentation</li> </ul>	1	2	a1, a2, b1, c1, c2, d1, d2
11	<ul style="list-style-type: none"> <li>- Final Practical Exam</li> </ul>	1	2	b1, c1, c2, d1
<b>Number of Weeks /and Units Per Semester</b>		<b>15</b>	<b>30</b>	

**C. Tutorial Aspect (if any):**

No.	Tutorial	Number of Weeks	Contact Hours	Learning Outcomes (CILOs)
1	NONE			
2				
3				
4				
5				
6				
7				
8				
9				
10				
11				
12				

No.	Tutorial	Number of Weeks	Contact Hours	Learning Outcomes (CILOs)
13				
14				
Number of Weeks /and Units Per Semester				

### VII. Assignments & Reports:

No.	Assignments	Week Due	Mark	Aligned CILOs (symbols)
1	Assignment 1: Memory addressing modes and Assembly instructions (Arithmetic & Logic)	4 <sup>th</sup> & 5 <sup>th</sup>	3	a1, a2, b1, d1
2	Assignment 2: Subroutines and Modular Programming in assembly	6 <sup>th</sup> & 7 <sup>th</sup>	3	a1, a2, b1, d1
3	Assignment 3: Interfacing 8086/8088-Mps, Report 1: Short Report on MP/MC-based Systems, Technologies, Programming and Simulation.	9 <sup>th</sup> to 11 <sup>th</sup>	5	a1, a2, b1, c1, d1, d2
4	Assignment 4: Interfacing with 8051-MC.	12 <sup>th</sup> & 13 <sup>th</sup>	4	a1, a2, b1, c1, d1, d2
Total			15	

### VIII. Schedule of Assessment Tasks for Students During the Semester:

No.	Assessment Method	Week Due	Mark	Proportion of Final Assessment	Aligned Course Learning Outcomes
1	Assignments & Reports	4 <sup>th</sup> to 13 <sup>th</sup>	15	10%	a1, a2, b1, c1, d1, d2

No.	Assessment Method	Week Due	Mark	Proportion of Final Assessment	Aligned Course Learning Outcomes
2	Quizzes 1 & 2	6 <sup>th</sup> & 12 <sup>th</sup>	10	6.67%	a1, a2, b1, c1, d1, d2
3	Mid-Term Practical Exam	7 <sup>th</sup>	15	10%	b1, c1, c2, d1
4	Mid-Term Theoretical Exam	8 <sup>th</sup>	20	13.33%	a1, a2, b1
5	Final Practical Exam including Project Presentation & Evaluation	15 <sup>th</sup>	30	20%	a1, a2, b1, c1, c2, d1, d2
6	Final Theoretical Exam	16 <sup>th</sup>	60	40%	a1, a2, b1
Total			150	100%	

## IX. Learning Resources:

- *Written in the following order:* Author, Year of publication, Title, Edition, Place of publication, Publisher.

### 1- Required Textbook(s) (maximum two):

- 1- M. Mazidi, and J. Mazidi, 2002, **The 80x86 IBM PC and Compatible Computers Assembly Language, Design and Interfacing**, 4<sup>th</sup> Edition, UK, Prentice Hall
- 2- M. Mazidi, and J. Mazidi, 2002, **The 8051 Microcontroller, Design and Interfacing**, 4<sup>th</sup> Edition, UK, Prentice Hall.

### 2- Essential References:

- 1- Richard C. Detmer, 2014, **Introduction to 80x86 Assembly Language and Computer Architecture**, 3<sup>rd</sup> edition, UK, Jones & Bartlett Learning.
- 1- Lyla B Das, 2010, **The X86 Microprocessors: Architecture and Programming (8086 to Pentium)**, 2<sup>nd</sup> Edition, New Delhi india, Dorling Kindersley.
- 2- John E. Uffenbeck, **The 80x86 Family: Design, Programming, and Interfacing.**

### 3- Electronic Materials and Web Sites etc.:

**Websites:**

**Courses:**

- 1- <http://nptel.iitm.ac.in>
- 2- <https://ocw.mit.edu/courses>

### Journals

1. <http://www.sciencedirect.com/>
2. <http://dl.acm.org/dl.cfm>
3. <http://ieeexplore.ieee.org/Xplore/guesthome.jsp>
4. <http://www.emeraldinsight.com>
5. <http://www.scopus.com/home.url>
6. <http://link.springer.com/>

### Other Web Sources:

- 1.....
- 2.....

## X. Course Policies: (Based on the Uniform Students' By law (2007))

1	<p><b>Class Attendance:</b></p> <p>Class Attendance is mandatory. A student is considered absent and shall be banned from taking the final exam if his/her absence exceeds 25% of total classes.</p>
2	<p><b>Tardiness:</b></p> <p>A student will be considered late if he/she is not in class after 10 minutes of the start time of class.</p>
3	<p><b>Exam Attendance/Punctuality:</b></p> <p>No student shall be allowed to the exam hall after 30 minutes of the start time, and shall not leave the hall before half of the exam time has passed.</p>
4	<p><b>Assignments &amp; Projects:</b></p> <p>Assignments and projects must be submitted on time. Students who delay their assignments or projects shall lose the mark allocated for the same.</p>
5	<p><b>Cheating:</b></p> <p>Cheating is an act of fraud that results in the cancelation of the student's exam or assignment. If it takes place in a final exam, the penalties stipulated for in the Uniform Students' Bylaw (2007) shall apply.</p>
6	<p><b>Forgery and Impersonation:</b></p> <p>Forgery/Impersonation is an act of fraud that results in the cancelation of the student's exam, assignment or project. If it takes place in a final exam, the penalties stipulated for in the Uniform Students' Bylaw (2007) shall apply.</p>
7	<p><b>Other policies:</b></p> <p>The University official regulations in force will be strictly observed and students shall comply with all rules and regulations of the examination set by the Department, Faculty and University Administration.</p>

## Faculty of Medical Technology

### Department of Biomedical Engineering

### Program of Biomedical Engineering

## Course Plan (Syllabus) of Microprocessors & Microcontrollers

Course Code. 07.02.722

2024/2025

I. Information about Faculty Member Responsible for the Course:							
Name of Faculty Member:	Assoc. Prof. Farouk Al-Fahaidy	Office Hours					
Location & Telephone No.:	777909815						
E-mail:	<a href="mailto:farouqakh@gmail.com">farouqakh@gmail.com</a>	SAT	SUN	MON	TUE	WED	THU

## II. Course Identification and General Information:

1	Course Title:	Microprocessors & Microcontrollers			
2	Course Code & Number:	07.02.722			
3	Credit Hours:	Credit Hours	Theory Hours		Lab. Hours
			Lecture	Exercise	
		3	2	--	2
4	Study Level/ Semester at which this Course is offered:	3 <sup>rd</sup> Level / 2 <sup>nd</sup> Semester			
5	Pre –Requisite (if any):	Digital Logic Design & Programming I			
6	Co –Requisite (if any):	None			
7	Program (s) in which the Course is Offered:	Bachelor of Biomedical Engineering			
8	Language of Teaching the Course:	Arabic & English			
9	Study System:				
10	Mode of Delivery:	Full Time			
11	Location of Teaching the Course:	Faculty of Medical Technology			
12	Prepared by:	Assoc. Prof. Farouk Al-Fahaidy			
13	Date of Approval:				

## III. Course Description:

This course aims to introduce the basic concepts and principles of microprocessors (MPs) and microcontrollers (MCs) based on their architectures, programming, and applications in the field of biomedical engineering. The MP/MC plays the main processing and controlling unit in different biomedical instrumentations. Topics covers, an introduction to MP/MC architectures & units, the 8086/8088-Mps internal architecture, the 8088-MP IC interfacing and assembly programming, the 8051-MC's GPIO ports interfacing & features and assembly/Micro-C programming. Throughout practical Lab experiments & computer-based lab works as well as, course project work, students will develop their practices skills in simulation, programming and

implementation of context-based systems using the MP/MC-based systems.

#### IV. Course Intended Learning Outcomes (CILOs) :

Upon successful completion of the Course, student will be able to:

##### A. Knowledge and Understanding:

- |    |  |
|----|--|
| a1 | Describe the theories and internal architecture of the microprocessors/ microcontrollers as well as their differences, features & capabilities.                        |
| a2 | Explain basic concepts and principles of MPs/MCs-based systems design, programming and applications in the field of biomedical engineering and healthcare environment. |

##### B. Intellectual Skills:

- |    |  |
|----|--|
| b1 | Solve healthcare domain problems related to MPs/MCs-based systems using suitable software packages, programming language, and proper electrical elements, devices & ICs. |
|----|--|

##### C. Professional and Practical Skills:

- |    |  |
|----|--|
| c1 | Use different programming environments & IDE in the development & integrating of controlling programs and for simulation of the suggested solutions while solving practices problems related to healthcare fields. |
| c2 | Carry-out lab & environmental experiments for maintenance and experimentation in biomedical engineering practices.   |

##### D. Transferable Skills:

- |    |  |
|----|--|
| d1 | Demonstrate the ability to work in stressful environments within different constraints |
| d2 | Communicate well in both orally and in written forms.                                  |

#### V. Course Contents:

##### A. Theoretical Aspect:

No.	Units/Topics List	Sub Topics List	Number of Weeks	Contact Hours
1	Introduction to MP/MC & Computing	<ul style="list-style-type: none"> <li>- Course Orientations, Aims &amp; Objectives,</li> <li>- Introduction to Microprocessors &amp; Microcontrollers,</li> </ul>	1	2

No.	Units/Topics List	Sub Topics List	Number of Weeks	Contact Hours
		– Numbering & Coding Systems.		
2	Internal Organization of Computer	– Introduction to Internal Organization of Computer, Relation between Internal organization, Internal working of computer, – Brief history of the 80x86 family, Inside the 8088/8086.	1	2
3	The 80x86 Microprocessor	– Introduction to the Assembly Programming, Program Segments, – The 80x86-MP's Memory Addressing Modes, Logical, Offset & Physical Addresses Calculation, Demonstration with Examples.	1	2
4	Assembly Language Programming	– Layout of Assembly Programs, Directives & A Sample Program, Assemble, link, and Run a Program, – Flag Register & Control Transfer Instructions, Data types and Definitions	1	2
5	Arithmetic and Logic Instructions and Programs	– Unsigned Addition & Subtraction, – Unsigned Multiplication & Division, – Logical & Shift Instructions and Sample Programs.	1	2
6	BCD, ASCII, Bios & Dos Programming in Assembly	– BCD & ASCII operands and Instructions, – Bios INT 10H Programming: using INT 10H, Dos Interrupt INT 21H: using INT 21H with different I/O Functions.	1	2
7	Strings & Tables and Macros & Modular Programming	– String Instructions & Look-up Tables Programming, – Define Macro in Assembly, Define the Local Variable in Macro, and including the Macro in another File – Modular Programming, Advantages, Break a Large Program into Modules, Code the Modules and calling the Program, EXTRN Directive, PUBLIC Directive, Link a Subprograms into one Executable Program.	1	2

No.	Units/Topics List	Sub Topics List	Number of Weeks	Contact Hours
8	Mid-Term Theoretical Exam	– ALL Previous Topics	1	2
9	The 8086/8088 MP IC & Interfacing with the 8255-IC	<ul style="list-style-type: none"> <li>– The 8086/8088-MP IC, Pins Functions, Modes of Operations, Memory &amp; I/O Ports Addressing &amp; Address Decoding Circuits,</li> <li>– Interfacing &amp; Programming of the 8088-Mp with I/O peripherals such as, Switch, LEDs and Sensors</li> <li>– The 8255-PPI IC, IC Pins, Configuration and Interfacing with Keypad &amp; LCD.</li> </ul>	2	4
10	Introduction to the 8051 Microcontroller Family & Programming	<ul style="list-style-type: none"> <li>– Introduction to 8051 family MCs, IC for 8051, features like ports, Timers, and Serial Modules, Internal RAM, General Purpose Registers (GPRs), Special Function Registers (SFRs),</li> <li>– Basic Assembly Programming Instructions,</li> <li>– Flowchart standard symbols.</li> </ul>	1	2
11	The 8051 Programming	<ul style="list-style-type: none"> <li>– Assembly Programming, Data Transfer Instructions, Addressing Modes, Data Processing Instructions, Program Branching Instructions, and Stack,</li> <li>– TIME DELAY Generation in 8051 MC.</li> </ul>	1	2
12	The 8051 Interfacing with Assembly Programming	<ul style="list-style-type: none"> <li>– The 8051-MC Interfacing, I/O Ports, LEDs, Seven-Segments, Switches, Keypad, PWM, DC-motor, Stepper motor &amp; Alphanumeric LCD</li> </ul> <p>ADC, DAC Modules.</p>	2	4
13	The 8051-MC's Interrupts	Interrupts Programming, Steps in executing an interrupt, Interrupt Sources, Interrupt Vectors, Interrupt Enable (IE) register, External interrupt, Interrupt Priorities.	1	2
14	Final Theoretical Exam	ALL Topics	1	2

No.	Units/Topics List	Sub Topics List	Number of Weeks	Contact Hours
Number of Weeks /and Units Per Semester			16	32

**B. Case Studies and Practical Aspect:**

No.	Tasks/ Experiments	Week Due	Contact Hours	
1	MP Lab & Computer based Lab Orientation: – Lap equipment and simulation Tools orientations – Installation of 8086/8088-Mps simulators and Proetus Simulator.	1	2	
2	– MOV and ADD assembly instructions illustrating memory addressing modes	1	2	
3	– Conditional Assembly Instructions, – Arithmetic, Shift & Logic & Shift Assembly Instructions.	2	4	
4	– BCD, ASCII, BIOS & Dos Assembly	1	2	
5	– String Assembly Instructions & Lookup Tables Programming	1	2	
6	– <b>Midterm Practical Exam</b>	1	2	
7	– Assembly programming: Macros and Modular Programming, – Building Assembly Program with Subroutines in Modular Programming.	2	4	
8	– The 8086/8088-Mps interfacing with Peripherals & Programming.	2	4	
9	– The 8051-MC Interfacing and Programming with Peripherals, – Demonstrating Interrupts Programming with 8051-MC.	2	4	
10	Projects Presentation	1	2	
11	Final Practical Exam	1	2	
Number of Weeks /and Units Per Semester			15	30

C. Tutorial Aspect:			
No.	Tutorial	Number of Weeks	Contact Hours
1			
2			
3			
4			
5			
6			
7			
8			
9			
10			
Number of Weeks /and Units Per Semester			

## VI. Teaching Strategies of the Course:

- Interactive lectures & examples,
- Interactive class discussions,
- Case studies,
- Exercises and home works,
- Laboratory/Practical experiments-based session,
- Computer laboratory-based sessions,
- Directed self- study,
- Problem based learning,
- Team work (cooperative learning),
- Project.

## VII. Assessment Methods of the Course:

- Written tests (mid and final terms and quizzes),

- Short reports,
- Lab\Project report
- Practical lab performance assessment,
- Coursework activities assessment,
- Home works and assignments,
- Presentations.

### VIII. Assignments:

No.	Assignments	Week Due	Mark
1	Assignment 1: Memory addressing modes and Assembly instructions (Arithmetic & Logic)	4 <sup>th</sup> & 5 <sup>th</sup>	3
2	Assignment 2: Subroutines and Modular Programming in assembly	6 <sup>th</sup> & 7 <sup>th</sup>	3
3	Assignment 3: Interfacing 8086/8088-Mps, Report 1: Short Report on MP/MC-based Systems, Technologies, Programming and Simulation.	9 <sup>th</sup> to 11 <sup>th</sup>	5
4	Assignment 4: Interfacing with 8051-MC.	12 <sup>th</sup> & 13 <sup>th</sup>	4
<b>Total</b>			<b>15</b>

### IX. Schedule of Assessment Tasks for Students During the Semester:

No.	Assessment Method	Week Due	Mark	Proportion of Final Assessment
1	Assignments & Reports	4 <sup>th</sup> to 13 <sup>th</sup>	15	10%
2	Quizzes 1 & 2	6 <sup>th</sup> & 12 <sup>th</sup>	10	6.67%
3	Mid-Term Practical Exam	7 <sup>th</sup>	15	10%
4	Mid-Term Theoretical Exam	8 <sup>th</sup>	20	13.33%
5	Final Practical Exam including Project Presentation & Evaluation	15 <sup>th</sup>	30	20%
6	Final Theoretical Exam	16 <sup>th</sup>	60	40%

No.	Assessment Method	Week Due	Mark	Proportion of Final Assessment
Total			150	100%

## X. Learning Resources:

- *Written in the following order: Author, Year of publication, Title, Edition, Place of publication, Publisher.*

### 1- Required Textbook(s) (maximum two):

- 1- M. Mazidi, and J. Mazidi, 2002, **The 80x86 IBM PC and Compatible Computers Assembly Language, Design and Interfacing**, 4<sup>th</sup> Edition, UK, Prentice Hall
- 1- M. Mazidi, and J. Mazidi, 2002, **The 8051 Microcontroller, Design and Interfacing**, 4<sup>th</sup> Edition, UK, Prentice Hall.

### 2- Essential References:

- 1- Richard C. Detmer, 2014, **Introduction to 80x86 Assembly Language and Computer Architecture**, 3<sup>rd</sup> edition, UK, Jones & Bartlett Learning.
- 1- Lyla B Das, 2010, **The X86 Microprocessors: Architecture and Programming (8086 to Pentium)**, 2<sup>nd</sup> Edition, New Delhi india, Dorling Kindersley.
- 2- John E. Uffenbeck, **The 80x86 Family: Design, Programming, and Interfacing.**

### 3- Electronic Materials and Web Sites etc.:

#### Websites:

#### Courses:

- 3- <http://nptel.iitm.ac.in>
- 4- <https://ocw.mit.edu/courses>

#### Journals

7. <http://www.sciencedirect.com/>
8. <http://dl.acm.org/dl.cfm>
9. <http://ieeexplore.ieee.org/Xplore/guesthome.jsp>
10. <http://www.emeraldinsight.com>
11. <http://www.scopus.com/home.url>
12. <http://link.springer.com/>

#### Other Web Sources:

- 1.....

<b>XI. Course Policies: (Based on the Uniform Students' Bylaw (2007))</b>	
1	<b>Class Attendance:</b> Class Attendance is mandatory. A student is considered absent and shall be banned from taking the final exam if his/her absence exceeds 25% of total classes.
2	<b>Tardiness:</b> A student will be considered late if he/she is not in class after 10 minutes of the start time of class.
3	<b>Exam Attendance/Punctuality:</b> No student shall be allowed to the exam hall after 30 minutes of the start time, and shall not leave the hall before half of the exam time has passed.
4	<b>Assignments &amp; Projects:</b> Assignments and projects must be submitted on time. Students who delay their assignments or projects shall lose the mark allocated for the same.
5	<b>Cheating:</b> Cheating is an act of fraud that results in the cancelation of the student's exam or assignment. If it takes place in a final exam, the penalties stipulated for in the Uniform Students' Bylaw (2007) shall apply.
6	<b>Forgery and Impersonation:</b> Forgery/Impersonation is an act of fraud that results in the cancelation of the student's exam, assignment or project. If it takes place in a final exam, the penalties stipulated for in the Uniform Students' Bylaw (2007) shall apply.
7	<b>Other policies:</b> The University official regulations in force will be strictly observed and students shall comply with all rules and regulations of the examination set by the Department, Faculty and University Administration.